



Johns Hopkins University
3400 North Charles Street
307 Malone Hall
Baltimore, MD 21218-2608

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To whom it may concern:

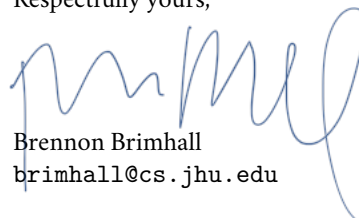
My name is Brennon Brimhall. I am an instructor and researcher in the Computer Science department at Johns Hopkins University. I am pleased to recommend Morgan Huberty for your internship program.

I first met Morgan in my Computer Systems Fundamentals class during the summer of 2025. This course is quite challenging during the normal semester; the summer schedule we have at Hopkins is compressed and requires even more effort from students. Topics for the class include x86 assembly, CPU pipelining, CPU caching, networking protocols, synchronization, concurrency, and more. I am pleased to report that Morgan successfully navigated the challenge with distinction. I also enjoyed after-class discussions with Morgan about Apple's transition from Intel to their own silicon; we spoke about the ISA tradeoffs, analyzed the M3/M4 design, and identified implications for compilers and software engineers.

Since our time together in class, Morgan has continued to focus in on systems topics. Notably, he has taken our FPGA Synthesis Lab, where he completed several advanced VHDL projects. I understand he has also been self-studying SystemVerilog on his own time. In addition to his solid technical foundation, I believe his continued learning outside the classroom speaks volumes about his grit, determination, and passion for self-directed learning.

Morgan tells me that he is most interested in an ASIC or FPGA engineering internship because he is fascinated by the tight interplay between software and hardware. In this regard, I believe that Morgan has reached the limit of what we can offer undergraduates in our department. Industry experience is the next step in his career. I firmly believe Morgan will prove an asset to any team.

Respectfully yours,



Brennon Brimhall
brimhall@cs.jhu.edu